INTEL® SOFTWARE DEVELOPER CONFERENCE - LONDON 2016

LEVEL 39, ONE CANADA SQUARE, CANARY WHARF

AGENDA DAY 1

WEDNESDAY NOVEMBER 30TH

TIME			MAIN SESSION
08:00	-	09:20	**Registration and light breakfast**
09:20		09:30	**Welcome & Introduction**
09:30	-	10:00	**From Legacy to Prodigy** Exploring strategies between code modernization, re-engineering and starting from scratch, to turn legacy applications into prodigy applications, again. **- Keynote by Tom Jonsthovel, Schlumberger**
10:00	-	10:30	**The Zeitgeist of Parallel Programming** **- Keynote by Robert Geva, Intel**
10:30	-	11:00	**Bringing AI and Machine Learning to Life with Intel** **- Keynote by Binay Ackalloor, Intel & Hanlin Tang, Intel (ex Nervana Systems)**
11:00		11:30	**Coffee Break**

SPLIT IN 2 TRACKS

TIME			TRACK 1 HPC DEVELOPMENT	TRACK 2 AI & MACHINE LEARNING
11:30	-	12:15	**Intel® Xeon Phi™: Fast, but no longer just an accelerator** James Cownie, Intel	**Introduction to Deep Learning** Hanlin Tang, Intel (ex Nervana Systems)
12:15	-	13:00	**Lunch Break**	
13:00	-	13:45	**Introducing FPGA for Software Developers** - and how to program the devices using OpenCL Graham McKenzie, Intel PSG	**Mapping Machine Learning to Hardware** – From the Edge to the Cloud Gaurav Kaul, Intel
13:45	-	14:30	**Optimizing SIMD codes with Intel Vectorization Advisor** Julia Fedorova, Intel	**Accelerating Machine Learning & Deep Learning on Intel® Architecture** Andrey Nikolaev, Intel
14:30		15:15	**Overview of the Intel® Threading Building Blocks (Intel® TBB) library** Olga Safonova, Intel	**High Performance Deep Learning on Intel® Architecture** – Hands-on with accelerated DL Frameworks Ivan Kuzmin, Intel
15:15	-	15:45	**Tea Break**	
15:45	-	16:30	**Getting insight into performance and scalability with Intel® VTune™ Amplifier XE** Dmitry Prohorov, Intel	**High Performance Machine Learning without the Pain: Intel® Distribution for Python*** Frank Schlimbach, Intel
16:30	-	17:00	**Deep-dive into Intel® MKL** Andrey Nikolaev, Intel	**A Deep Dive on Deep Learning** Hanlin Tang, Intel (ex Nervana Systems)
17:00	-	17:45	**Accelerating high speed analytics of tick-by-tick market data using KX's KDB and Intel Lustre** Gabriele Paciucci, Intel / Glenn Wright, Kx Systems	**Scaling Deep Learning from Training to Deployment – Introducing the Intel DL SDK** Ran A. Cohen, Intel
17:45	-	18:15	**Panel discussion with the speakers**	**FPGA for Machine Inference –** How FPGA can be used for power efficient CNN classification. Graham McKenzie, Intel PSG
18:15	-	20:00	**Networking evening with drinks & finger food**	

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AGENDA DAY 2

THURSDAY DECEMBER 1ST

TIME	HPC FOR FINANCE	HPC CODE MODERNIZATION WORKSHOP
08:00 - 09:30	**Registration and light breakfast**	
09:30 - 10:00	**Opening Session** Robert Geva, Intel	**Scaling up Deep Learning on Intel® Xeon Phi™ (KNL)** David Lecomber, Allinea
10:00 - 10:30	**Customer Case Study: RBS** Jitendra Shah & Tim Meadowcroft, RBS	**HPC - Code Modernization: - What's the challenge?** Stephen Blair-Chappell, Bayncore
10:30 - 11:15	**Intel Xeon Servers platform Update** Dheemanth Nagaraj, Intel	**HPC Performance & Compatibility: Delving into the mysteries of CPU Dispatch and how to harness it to your advantage** Stephen Blair-Chappell, Bayncore
11:15 - 11:45	**Coffee Break**	
11:45 - 12:30	**Deep-dive into Intel® MKL to create financial applications** Andrey Nikolaev, Intel	**HPC Case Study: Optimization of a stencil code for Xeon Phi Knights Landing** Janko Strassburg, Bayncore
12:30 - 13:00	**MCDRAM vs DDR on KNL: tuning for performance** Evgueny Khartchenko, Intel	**HPC Low-level custom application analysis using Intel® VTune Amplifier** Stephen Blair-Chappell, Bayncore
13:00 - 14:00	**Lunch Break**	
14:00 - 14:30	**Intel® TBB preview: Heterogeneous Flow Graph (STAC-A2)** Alex Katranov, Intel	**Prepare for the latest generation of Intel Xeon Phi architecture before having access to the hardware** Janko Strassburg, Bayncore
14:30 - 15:00	**Case Study: BestX – Intel® Python & Machine Learning for Best Execution (FSI)** Aman Thind, Co-Founder & Director, BestX Ltd	**HPC Profiling & Analysis: In-depth Vectorisation - A hands-on session using the Intel Vector Advisor** Roger Philp, Bayncore
15:00 - 15:30	**Intel® TBB preview: Parallel (Vector) STL** Alex Katranov, Intel	**HPC Case Study: The N-body problem on Xeon and Xeon Phi Knights Landing** Roger Philp, Bayncore
15:30 - 16:15	**Optimizing SIMD codes with Intel Vectorization Advisor** Julia Fedorova, Intel	
16:15 - 16:45	**Tea Break**	
16:45 - 17:15	**When High Performance Joins Productivity: Intel® Distribution for Python*** Frank Schlimbach, Intel	**HPC Performance & Compatibility: So who says Python is slow? Hands-on demo Using the full power of Intel's optimized Python Distribution** Stephen Blair-Chappell, Bayncore
17:15 - 17:45	**Achieving very high performance data analytics with the Intel Knights Landing (KNL) platform** Glenn Wright, Kx Systems	**HPC Performance & Compatibility: Floating point calculations and the problem of bit wise reproducibility.** Stephen Blair-Chappell, Bayncore
17:45 - 18:15	**Getting the maximum performance out of Solid State Drives** Evgueny Khartchenko, Intel	**Open Forum with the speakers** Stephen Blair-Chappell, Janko Strassburg, Bayncore
18:15	**Closing**	