



## Advanced Topics for Developers - New Agenda!

2-DAYS WORKSHOP IN ROME: 23<sup>rd</sup> & 24<sup>th</sup> May 2017

Best Western Plus Hotel Universo, Via Principe Amedeo 5/B - 00185 – Roma

More information and registration: <http://www.inteldevconference.com>

### DAY 1 AGENDA

TUESDAY 23.5.2017

#### THEME OF THE DAY: OPTIMISATION TECHNIQUES USING INTEL PARALLEL STUDIO XE

08:30 09:30 Registration and Breakfast

09:30 09:40 Welcome – Day 1

09:40 10:10 **USING THE ADVANCED FEATURES OF INTEL PARALLEL STUDIO - WHAT'S ON OFFER**  
A quick overview of some of the more advanced features of Intel Parallel Studio XE and what's new in the latest version.  
Presenter: Stephen Blair-Chappell, Bayncore

10:10 11:00 **CASE STUDY**  
An example of how advanced optimisation techniques along with the Intel Parallel Studio XE have been used in a real-world application.  
Presenter: Francois Fayard, Bayncore

11:00 11:30 Coffee Break

11:30 12:00 **SOMETHING OLD, SOMETHING NEW, SOMETHING BORROWED, SOMETHING BLUE**  
A look at four important features that make Intel x86 architecture 'best in class'  
Presenter: Stephen Blair-Chappell, Bayncore

12:00 12:45 **ARCHITECTURAL ANALYSIS USING VTUNE**  
In this session, we take a 'deep dive' into hardware *event based analysis* using Intel VTune Amplifier XE. We present a classic 'top down' architectural analysis methodology and show how such techniques can become your method of choice when carrying out both course-grained and fine-grained code optimization.  
Presenter: Stephen Blair-Chappell, Bayncore

12:45 13:45 Lunch

13:45 14:45 **ARCHITECTURAL ANALYSIS USING VTUNE (Continued)**

14:45 15:15 **ROOFLINE PROFILING WITH INTEL VECTOR ADVISOR**  
Learn how to use Intel Vector Advisor's roofline analysis to answer such questions as: Does my application work optimally on the current hardware? What is the most underutilized hardware resource? What limits performance? Is my application workload memory or compute bound? What is the right strategy to improve application performance?  
Presenter: Francois Fayard, Bayncore

15:15 15:45 Coffee Break

15:45 16:30 **USING THE INTEL VECTOR ADVISOR**  
In this session, we show how to use Intel Vector Advisor to check how well your code is being vectorised. Additionally, we look at various memory issues, such as non-contiguous memory accesses and unit stride vs. non-unit stride accesses, and how eliminating such issues can lead to significant speed up of vectorised code and improve the quality of code generated automatically by the compiler.  
Presenter: Roger Philp, Bayncore

16:30 17:15 **ADVANCED CUSTOM ANALYSIS AND CONFIGURATION USING VTUNE**  
Using Intel VTune Amplifier XE 'out of the box' produces excellent results, however, there are occasions when by using your own custom configuration you can directly influence quality of the analysis. In this session, we show how to create and use your own custom analysis types; choose what hardware events to use; modify the collection parameters; and add API calls to your source code - leading to a better analysis and help track down those difficult-to-find bottlenecks in your code.  
Presenter: Stephen Blair-Chappell, Bayncore

17:15 17:30 Q&A

17:30 19:30 Networking with drinks & finger food



# INTEL® CODE MODERNIZATION WORKSHOP - 2017

## DAY 2 AGENDA

WEDNESDAY 24.5. 2017

THEME OF THE DAY: PRACTICAL MACHINE LEARNING AND DEEP LEARNING ON INTEL PLATFORMS

08:30 09:30 Breakfast

### 09:30 10:15 CPU DISPATCH

In-depth exploration of the CPU Dispatching mechanism provided by the Intel Compilers.  
Presenter: Stephen Blair-Chappell, Bayncore

### 10:15 11:00 HOW TO SUPER-CHARGE YOUR PYTHON CODES USING INTEL OPTIMISED PYTHON

See how the latest Python distribution from Intel brings a significant performance boost in AI and Deep Learning/Machine Learning codes.  
Presenter: Stephen Blair-Chappell, Bayncore

11:00 11:30 Coffee Break

### 11:30 12:45 BENCHMARKING AND ANALYSIS OF KEY MACHINE LEARNING PATTERNS AND KERNELS

In this session, we demonstrate the analysis of some classic ML patterns (e.g. SVM, K-Means and CNN) and computational kernels (e.g. GEMM, SVD PCA, etc.) , and show how they can be optimized to produce superior performance.  
Presenter: Francois Fayard, Bayncore

12:45 13:45 Lunch Break

### 13:45 14:45 PRACTICAL FRAMEWORKS SESSION 1: USING CAFFE ON INTEL ARCHITECTURE

In this session we show how to build Caffe optimized for Intel architecture, train deep network models using one or more compute nodes, and deploy networks. In addition, various functionalities of Caffe are explored in detail including how to fine-tune, extract and view features of different models, and use the Caffe Python API.  
Presenter: Francois Fayard, Bayncore

### 14:45 15:30 PRACTICAL FRAMEWORKS SESSION 2: APPLICATION DEVELOPMENT USING KERAS, TENSORFLOW AND THEANO

In this tutorial we show how to use the Intel-optimized versions *TensorFlow* and *Theano* hosted on the high-level neural networks library *Keras*. As well as demonstrating of how to use these frameworks, the session will include a 'live' VTune analysis of the frameworks and an explanation of how the Intel implemented optimizations were achieved.  
Presenter: Stephen Blair-Chappell, Bayncore

15:30 15:45 Coffee Break

### 15:45 17:00 ACCELERATING MACHINE LEARNING & CASE STUDY

A quick overview of the latest tools and technologies available from Intel in Artificial Intelligence and an associated Case Study.  
Presenter: Roger Philp, Bayncore

17:00 17:15 Q&A

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